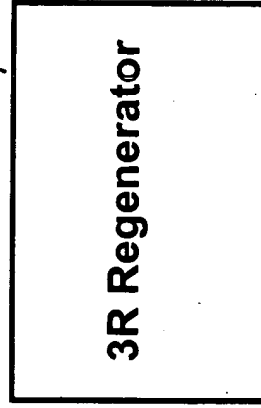


P_{in} (Input-Signal)



P_{clk} (Clock-Signal)



$\sim 130^\circ$

P_{reg} (Regenerated-Signal)



Figure 1

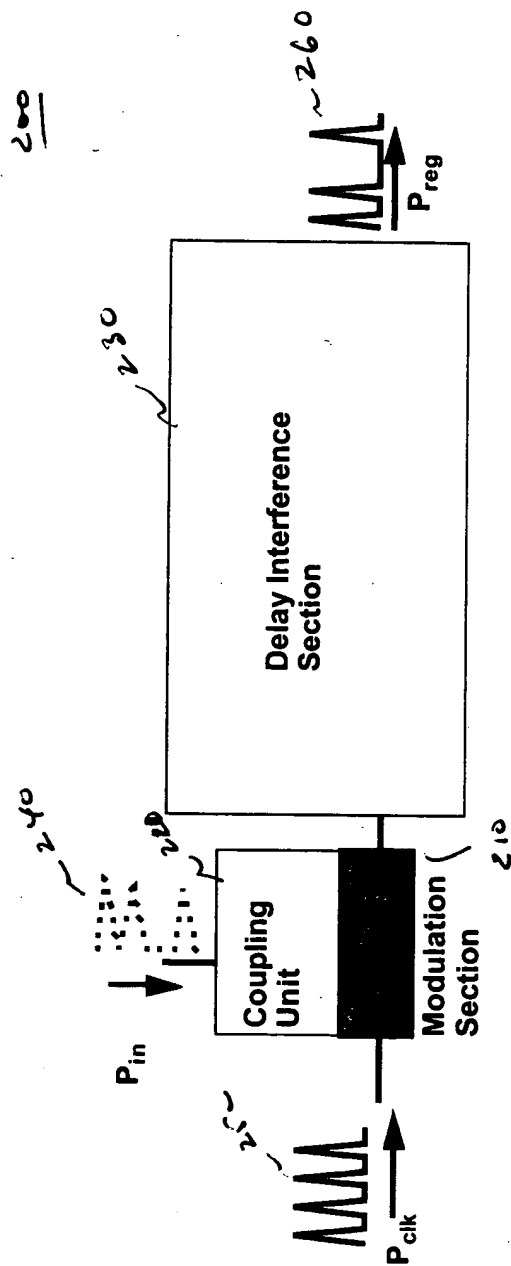


FIGURE 2

09745785-12200

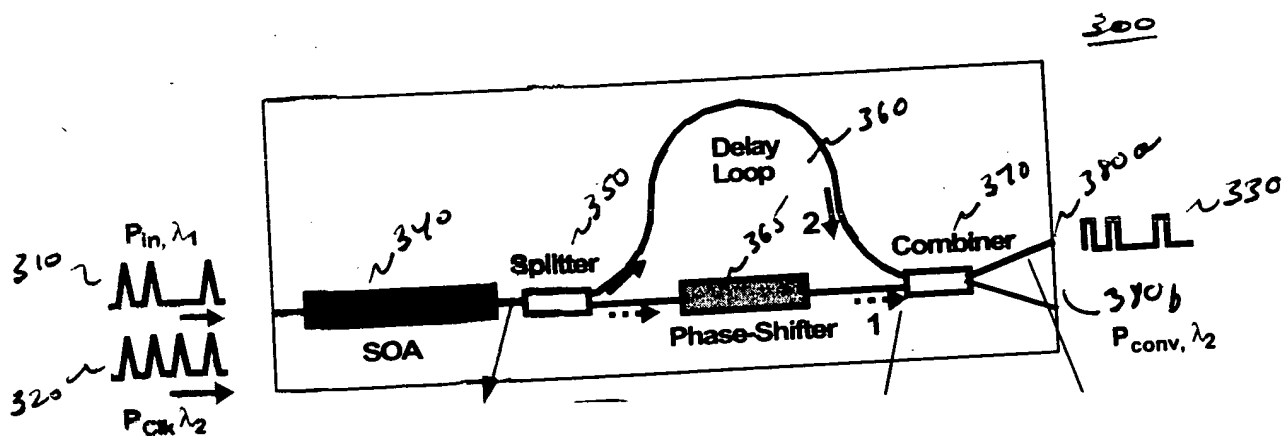


Figure 3

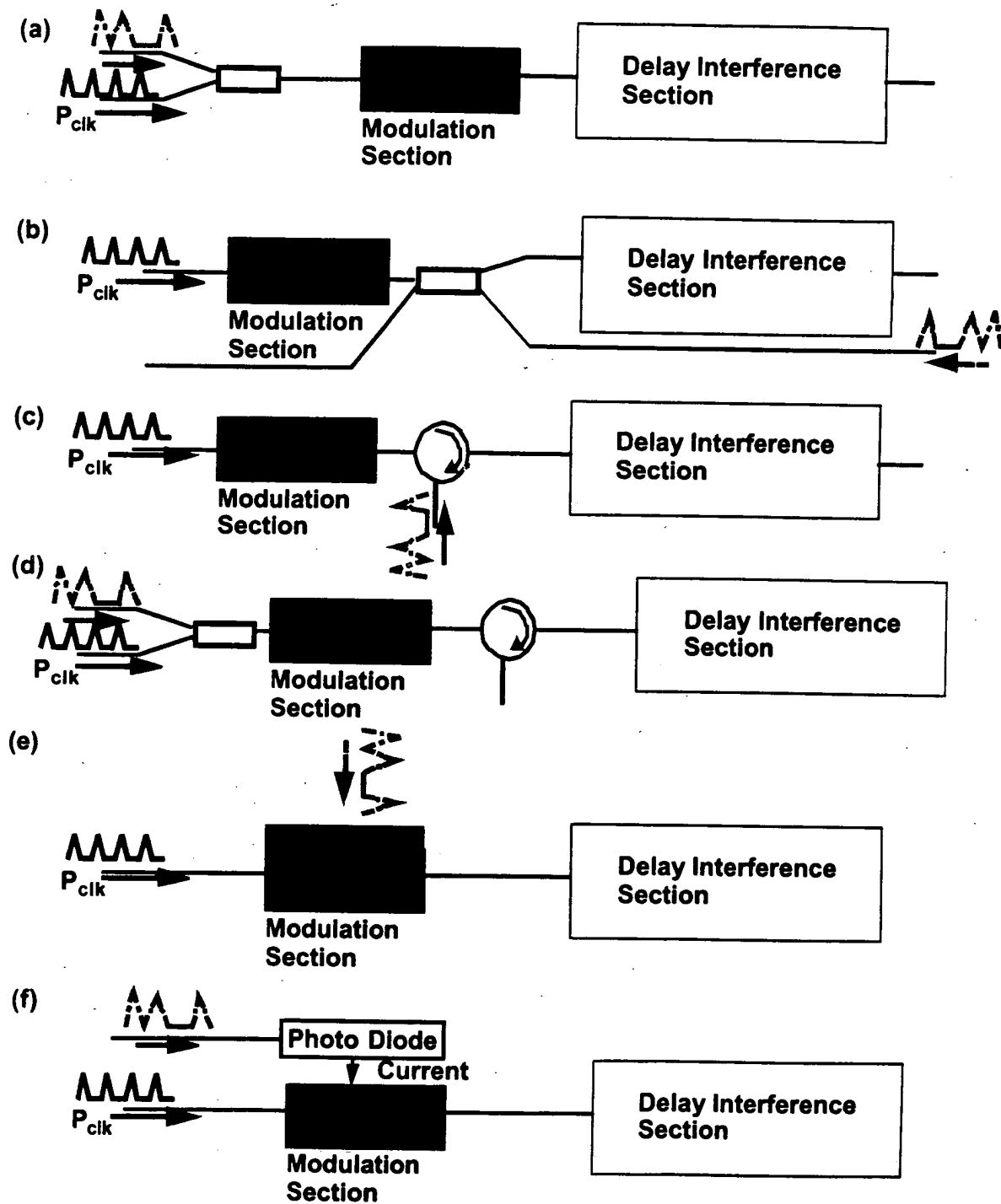
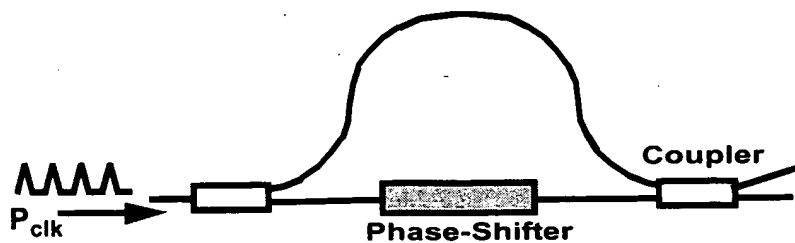


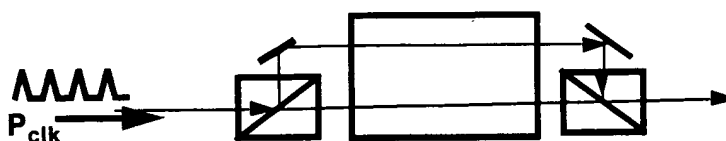
Figure 4

002227 5825460

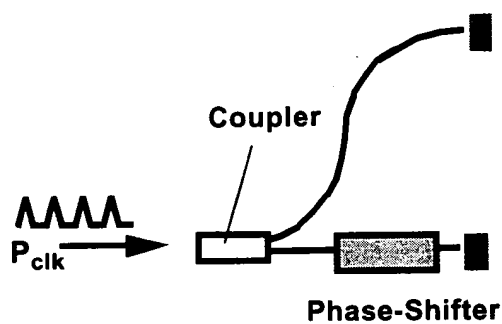
(a₁)



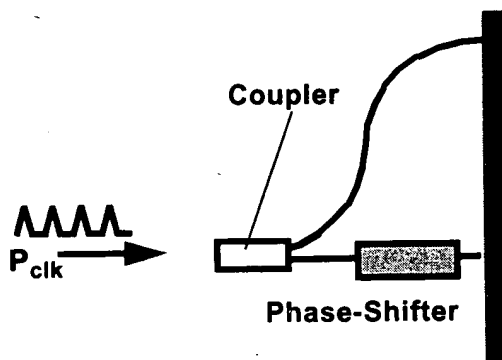
(a₂)



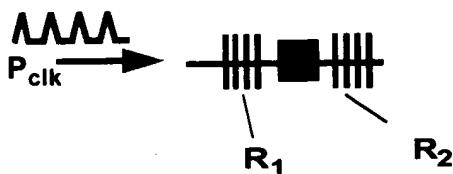
(b)



(c)



(d)



(e)

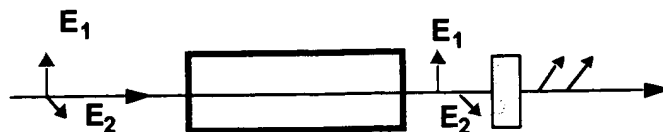


Figure 5

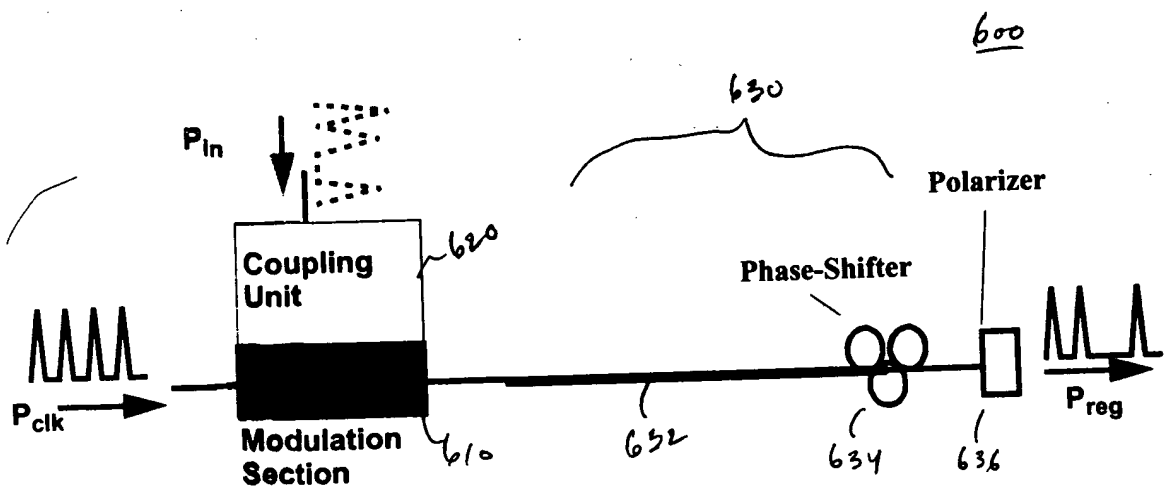


Figure 6(a)

09745785-12200

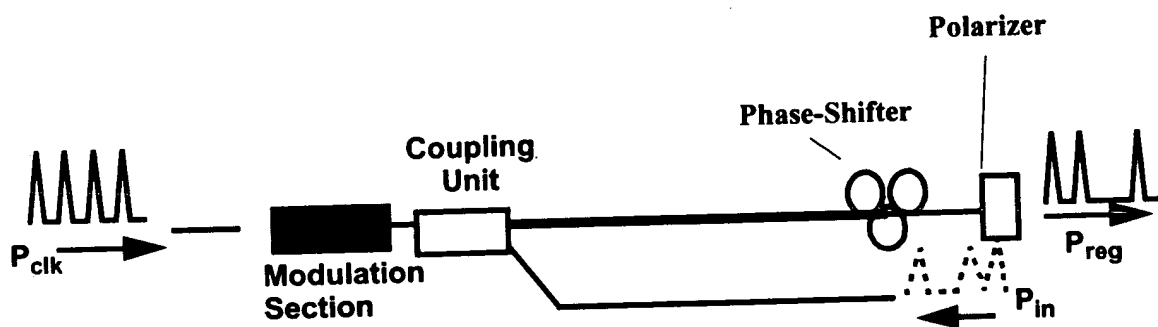


Figure 6(b)